

Atty.
Dkt. No.

224665

Client Ref.

P3334B/SS

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Date: August 5, 1996

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Applicant: LEE

Cont. of Appln. No.: 08/596,423

Filing Date: August 5, 1996

Examiner: T. Cunningham

Group Art Unit: 2504

U.S. PATENT DOCUMENTS

Examiner's Initials*		Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	SubClass	Filing Date (if appropriate)
TDC	AR	5,157,278	10/1992	Min et al.			
	BR						
	CR						
	DR						
	ER						
	FR						
	GR						
	HR						
	IR						
	JR						
	KR						
	LR						
	MR						
	NR						

FOREIGN PATENT DOCUMENTS

		Document Number	Date MM/YYYY	Country	Inventor Name	Class	SubClass	English Abstract		Translation Readily Available	
								Enclosed	No	Enclosed	No
	OR	93-13276	07/1993	Korea							
	PR										
	QR										
	RR										
	SR										
	TR										
	UR										
	VR										
	WR										
	XR										

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

TDC	YR	Low Power Self Refresh Mode With Temperature Detecting Circuit, 1993 SYMPOSIUM ON VLSI CIRCUITS, pp. 43-44, Kagenishi et al.									
	ZR										
	AAR										
	ABR										
	ACR										
	ADR										

Examiner T. Cunningham

Date Considered: 9/26/96

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.